IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kay HELLIG et al.

Assignee: Advanced Micro Devices, Inc.

Title: SYSTEM FOR FORMING A SEMICONDUCTOR DEVICE AND METHOD

THEREOF

Patent No.: 7,256,113 B1 Issued: August 14, 2007

Atty. Docket No.: 1458-TT4978

MS: Certificate of Correction Branch COMMISSIONER FOR PATENTS PO Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT— PTO MISTAKE (37 C.F.R. § 1.322(a))

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake to be corrected was made by the Office.

- 1. Attached hereto is Form PTO-1050, with at least one copy suitable for printing.
- 2. The exact page and line number where the error is shown correctly in the application file:

Request for Continued Examination filed 02/27/2004, page 6, claim number 30 (renumbered as claim 21 in granted patent)

3. Please send the Certificate to:

J. GUSTAV LARSON

LARSON NEWMAN ABEL POLANSKY & WHITE, LLP 5914 WEST COURTYARD DRIVE, SUITE 200 AUSTIN, TEXAS 78730

February 29, 2008

Date

Respectfully submitted, /J. Gustav Larson/

J. Gustav Larson, Reg. No. 39,263

Larson Newman Abel Polansky & White, LLP 5914 West Courtyard Drive, Suite 200

Austin, Texas 78730

512-439-7100 (phone) 512-439-7199 (fax)